

CLOCK BUFFER, 200MHZ, -40 TO 105DEG C New

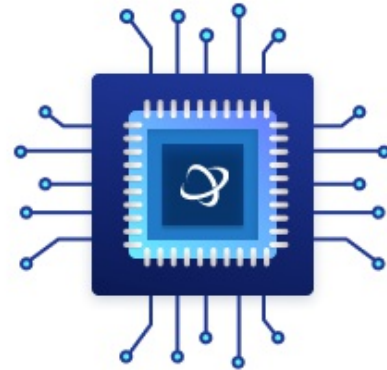
Manufacturers [Renesas Technology Corp](#)

Package/Case COL-8

Product Type Integrated Circuits (ICs)

RoHS

Lifecycle



Images are for reference only

Please submit RFQ for 551SCMGI or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The 551S is a low cost, high-speed single input to four output LVCMOS clock buffer. The 551S has best in class Additive Phase Jitter of sub 50 fsec.

Features

Low additive phase jitter RMS: 50 fs

Extremely low skew outputs (50 ps)

Low cost clock buffer

Packaged in 8-pin SOIC and 8-pin DFN, Pb-free

Input / Output clock frequency up to 200 MHz

Non-inverting output clock

Ideal for networking clocks

Operating Voltages: 1.8 V to 3.3 V

Output Enable mode tri-states outputs

Advanced, low power CMOS process

Extended temperature range (-40°C to +105°C)

Related Products



[HD64F7055SF40K](#)

Renesas Technology Corp
QFP256



[HD64F7055RFJ40](#)

Renesas Technology Corp
QFP-300



[ISL95855HRTZ-T](#)

Renesas Technology Corp
48pin-TQFN



[M5M51008DFP-55HIBT](#)

Renesas Technology Corp
SOP-32



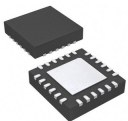
[HD64F7055F40V](#)

Renesas Technology Corp
PQFP-256



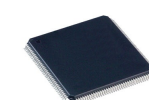
[ISL95855IRTZ-T](#)

Renesas Technology Corp
TQFN



[ISL95855IRTZ](#)

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TQFN-48



[R5F70855AN80FPV](#)

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TQFP144