

128 MACROCELL 1.8V ZERO POWER ISP CPLD

Manufacturers	AMD Xilinx, Inc
Package/Case	100-TQFP
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for XC2C128-6VQG100C or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

XC2C128-6VQG100C is a Field-Programmable Gate Array (FPGA) from Xilinx. Here are some of its features:

Features

- It has 128,000 system gates.
- It operates at a maximum frequency of 166 MHz.
- It has 4,096 logic cells and 16 digital signal processing (DSP) slices.
- It has 216 kilobits of block RAM and 78 I/O pins.
- It has a 1.8V to 3.3V operating voltage range.

Application

- Digital signal processing
- Video processing and imaging
- Control systems
- Communication systems
- High-performance computing



Related Products



[XC18V01S020C](#)

AMD Xilinx, Inc
SOP-20



[XCF04SV0G20C](#)

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[XCV50-6BG256C](#)

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[XC6SLX16-3CSG324C](#)

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