

MIC94325YMT-TR

Data Sheet

Linear Voltage Regulator, 500mA, Adjustable, 1.2 3.4 V, ±3% 6-Pin, DFN

Manufacturers <u>Microchip Technology</u>, Inc

Package/Case UDFN-6

Product Type Power Management ICs

RoHS

Lifecycle



Images are for reference only

Please submit RFQ for MIC94325YMT-TR or Email to us: sales@ovaga.com We will contact you in 12 hours.

RFO

General Description

The MIC943x5 Ripple BlockerTM is a monolithic integrated circuit that provides low-frequency ripple attenuation (switching noise rejection) to a regulated output voltage. This is important for applications where a DC/DC switching converter is required to lower or raise a battery voltage but where switching noise cannot be tolerated by sensitive downstream circuits such as in RF applications. The MIC943x5 maintains high power supply ripple rejection (PSRR) with input voltages operating near the output voltage level to improve overall system efficiency. A low-voltage logic enable pin facilitates ON/OFF control at typical GPIO voltage levels.

The MIC943x5 operates from an input voltage of 1.8V to 3.6V. The MIC943x5 options include fixed (MIC94345/55) or adjustable (MIC94325) output voltages. The MIC94355 version offers an auto-discharge to discharge the output capacitor when the part is disabled.

Packaged in a 6-pin 1.6mm x 1.6mm Thin DFN, the MIC943x5 has a junction operating temperature range of -40°C to +125°C.

Features

1.8V to 3.6V input voltage range

Active noise rejection over a wide frequency band: >50dB from 10Hz to 5MHz at 500mA load

Fixed and adjustable output voltages

Optional output auto-discharge when disabled



Related Products



MIC4684YM

Microchip Technology, Inc

SOIC-8



MIC2090-1YM5-TR
Microchip Technology, Inc
SOT-23-5



MIC2009A-1YM6-TR

Microchip Technology, Inc
SOT-23-6



MIC5841YWM-TR
Microchip Technology, Inc
SOIC-18



MIC5891YN

Microchip Technology, Inc PDIP-16



MIC29152WT

Microchip Technology, Inc TO-220-5



MIC5209YM

Microchip Technology, Inc
SOIC-8



MIC2026-2YM

Microchip Technology, Inc
SOIC-8