

FIFO 128Kx36 3.3V SUPER SYNC II FIFO

Manufacturers	Renesas Technology Corp
Package/Case	TQFP-100
Product Type	Memory
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for 72V36110L10PF or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

RFQ

General Description

The 72V36110 128K x 36 SuperSync II FIFO memory with clocked read and write controls offers flexible Bus-Matching x36/x18/x9 data flow and Asynchronous/Synchronous translation on the read or write ports ; SuperSync II FIFO's are appropriate for network, video, telecommunications, data communications and other applications that need to buffer large amounts of data and match busses of unequal sizes.

Features

Pin compatible to the SuperSync II 72V36x0 family

Up to 166 MHz Operation of the Clocks

User selectable Asynchronous read and/or write ports (PBGA Only)

User selectable input and output port bus-sizing

5V input tolerant

Auto power down minimizes standby power consumption

Master Reset clears entire FIFO

Partial Reset clears data, but retains programmable settings

Easily expandable in depth and width

JTAG port, provided for Boundary Scan function (PBGA Only)

Independent Read and Write Clocks

Available in 128-pin TQFP or a 144-pin PBGA packages

Industrial temperature range (-40C to +85C) is available



Related Products



[7205L15J](#)

Renesas Technology Corp
PLC32



[7204L25JI](#)

Renesas Technology Corp
32-LCC (J-Lead)



[72V275L15PF](#)

Renesas Technology Corp
TQFP-64



[72V2113L10PFI](#)

Renesas Technology Corp
TQFP-80



[7201LA50TDB](#)

Renesas Technology Corp
CDIP28



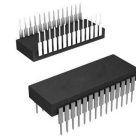
[72V261LA10TFG](#)

Renesas Technology Corp
64-LQFP



[7204L12J](#)

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32-LCC (J-Lead)



[7207L20DB](#)

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