

TinyLogic UHS D-Type Latch with 3-STATE Output; Package: SC70; No of Pins: 6;
Container: Tape & Reel,Latches UHS D-Type Latch

Manufacturers	ON Semiconductor, LLC
Package/Case	SC-70-6
Product Type	Logic ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for NC7SZ373P6X or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.



General Description

The NC7SZ373 is a single positive edge-triggered D-type CMOS Latch with 3-STATE output from Fairchild's Ultra High Speed Series of TinyLogic® in the space saving SC70 6-lead package. The device is fabricated with advanced CMOS technology to achieve ultra high speed with high output drive while maintaining low static power dissipation over a very broad VCC operating range. The device is specified to operate over the 1.65V to 5.5V range. The inputs and output are high impedance when VCC is 0V. Inputs tolerate voltages up to 7V independent of VCC operating voltage. The latch appears transparent to the data when Latch Enable (LE) is HIGH. When LE is LOW, the data that meets the setup time is latched. The output tolerates voltages above VCC in the 3-STATE condition.

Application

ONSEMI

Related Products



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SC-70-6



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SC-70