

2-Output Very Low Power PCIe Gen1-2-3 Clock Generator; HT SUS4
 CODE:8542390000

Manufacturers	<u>Renesas Technology Corp</u>
Package/Case	24-VFQFN
Product Type	Clock & Timer ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for 9FGV0241AKILF or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.



General Description

The 9FGV0241 is a 2-output very low power frequency generator for PCIe Gen1-4 applications with integrated output terminations providing

Features

PCIe Gen1-4 compliant

Integrated terminations provide 100 Ω differential Z_0 : reduced component count and board space

1.8 V operation: reduced power consumption

OE# pins: support DIF power management

LP-HCSL differential clock outputs: reduced power and board space

Programmable slew rate for each output: allows tuning for various line lengths

Programmable output amplitude: allows tuning for various application environments

DIF outputs blocked until PLL is locked: clean system start-up

Selectable 0%, -0.25% or -0.5% spread on DIF outputs: reduces EMI

External 25 MHz crystal; supports tight ppm with 0 ppm synthesis error

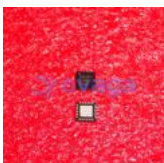
Configuration can be accomplished with strapping pins: SMBus interface not required for device control

3.3 V tolerant SMBus interface works with legacy controllers

Space-saving 4x4 mm 24-pin VFQFPN; minimal board space



Related Products



[9FGV0241AKILF](#)

Renesas Technology Corp
VFQFPN-24



[49FCT3805QG](#)

Renesas Technology Corp
QSOP-20



[9FGV0841AKLFT](#)

Renesas Technology Corp
48-VFQFN

[49FCT3805APYGI](#)

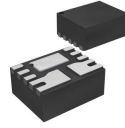


Renesas Technology Corp
20-SSOP (0.209", 5.30mm Width)



[49FCT3805ASOGI](#)

Renesas Technology Corp
20-SOIC (0.295", 7.50mm Width)



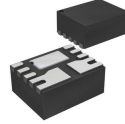
[9FGV0841AKLF](#)

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