

XC7K325T-1FFG900C

Data Sheet

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FPGA Kintex-7 Family 326080 Cells 28nm Technology 1V

Manufacturers	AMD Xilinx, Inc		
Package/Case	BGA-900		
Product Type	Programmable Logic ICs	Images are for reference only	
RoHS			
Lifecycle			
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Please submit RFQ for XC7K325T-1FFG900C or <u>Email to us: sales@ovaga.com</u> We will contact you in 12 RFO			

General Description

hours.

XC7K325T-1FFG900C is a product code for a specific model of Xilinx's 7-series field-programmable gate array (FPGA) device. Here's a breakdown of its components:

Features	Application
"XC" stands for Xilinx FPGA	325,200 logic cells
"7" stands for the 7-series FPGA family	21,560 Kb block RAM
"K" stands for the Kintex-7 sub-family	1,620 DSP slices
"325" refers to the number of logic cells (i.e. basic building blocks) in the FPGA	520 I/O pins
"T" indicates that it is designed for a specific temperature range (i.e. commercial grade)	Maximum clock frequency of 400 MHz
"-1" indicates the speed grade of the device	Configurable logic block (CLB) architecture

"FFG900C" refers to the package type, pin count, and speed grade.



Related Products



XC18V01S020C AMD Xilinx, Inc SOP-20



XCF04SV0G20C AMD Xilinx, Inc

TSSOP20



XC6SLX4-2CSG225C AMD Xilinx, Inc BGA-225



XCV50-6BG256C

AMD Xilinx, Inc BGA256



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XCF08PV0G48C

AMD Xilinx, Inc TSOP-48

XC6SLX25-3FTG256C

AMD Xilinx, Inc BGA-256

XC6SLX16-3CSG324C

AMD Xilinx, Inc BGA-324

AMD Xilinx, Inc

AMD Xilinx, Inc TSOP48





