

# EPM7256SRC208-10N

Data Sheet

CPLD MAX® 7000 Family 5K Gates 256 Macro Cells 100MHz 5V 208-Pin RQFP Tray

Manufacturers <u>Altera Corporation (Intel)</u>

Package/Case PQFP-208

Product Type Programmable Logic ICs

RoHS Rohs

ADERA MAX PATRICULAR COLLARS ACCOUNTS A

Images are for reference only

Lifecycle

Please submit RFQ for EPM7256SRC208-10N or <a href="mailto:sales@ovaga.com"><u>Emailto:sales@ovaga.com</u></a> We will contact you in 12 hours.

**RFO** 

# **General Description**

EPM7256SRC208-10N is a specific model of programmable logic device (PLD) manufactured by Intel Corporation.

# Features Application

256 macrocells Control and data processing in various electronic systems, such as telecommunications

equipment, industrial automation, and automotive electronics 10 ns maximum propagation delay

ASIC prototyping and emulation

208-pin plastic quad flat pack (PQFP) package

Replacement of multiple discrete logic devices with a single PLD, reducing board space and

5V power supply complexity

In-system programmable (ISP) through the Joint Test Action Group (JTAG) interface



### **Related Products**



EP4CE55F29C8N

Altera Corporation (Intel) FBGA-780



**EPM1270T144A5N** 

Altera Corporation (Intel) TQFP-144



**EP2C35F672C8N** 

Altera Corporation (Intel) FBGA-672



**EP2C35F484C7N** 

Altera Corporation (Intel) FBGA-484



# **EPM240M100C5N**

Altera Corporation (Intel) BGA-100



## EPM570F256C5N

Altera Corporation (Intel) FBGA-256



### **EPM7128AETC100-10**

Altera Corporation (Intel)
TQFP-100



#### **EP2C35F484I8N**

Altera Corporation (Intel) FBGA-484