

Analogue Switch, 3 Channels, SPDT, 85 ohm, 2V to 12V, TSSOP, 16 Pins

Manufacturers	Analog Devices, Inc
Package/Case	TSSOP-16
Product Type	Interface - Switches, Multiplexers, Demultiplexers
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for ADG633YRUZ or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The ADG633 is a low voltage CMOS device comprising three independently selectable single-pole, double-throw (SPDT) switches. The device is fully specified for ± 5 V, +5 V, and +3 V supplies. The ADG633 switches are turned on with a logic low (or high) on the appropriate control input. Each switch conducts equally well in both directions when on and has an input signal range that extends to the supplies. An EN input is used to enable or disable the device. When the device is disabled, all channels are switched off.

The ADG633 is designed on an enhanced process that provides lower power dissipation, yet is capable of high switching speeds. Low power consumption and an operating supply range of 2 V to 12 V make the ADG633 ideal for battery-powered, portable instruments. All channels exhibit break-before-make switching action, preventing momentary shorting when switching channels.

All digital inputs have 0.8 V to 2.4 V logic thresholds, ensuring TTL/CMOS logic compatibility when using single +5 V or dual ± 5 V supplies.

The ADG633 is available in a small, 16-lead TSSOP package and a 16-lead, 4 mm \times 4 mm LFCSP package.

Product Highlights

Single- and dual-supply operation. The ADG633 offers high performance and is fully specified and guaranteed with ± 5 V, +5 V, and +3 V supply rails.

Temperature range: -40°C to $+125^{\circ}\text{C}$.

Guaranteed break-before-make switching action.

Low power consumption, typically $<0.1 \mu\text{W}$.

Small, 16-lead TSSOP and 16-lead, 4 mm \times 4 mm LFCSP packages.

Features

2 V to 12 V single-supply operation

Temperature range: -40°C to $+125^{\circ}\text{C}$

52 Ω on resistance over full signal range

Rail-to-rail switching operation

16-lead LFCSP and TSSOP packages

Typical power consumption: $<0.1 \mu\text{W}$

TTL-/CMOS-compatible inputs

Package upgrades to 74HC4053 and MAX4053/MAX4583

Application

Automatic test equipment

Data acquisition systems

Battery-powered systems

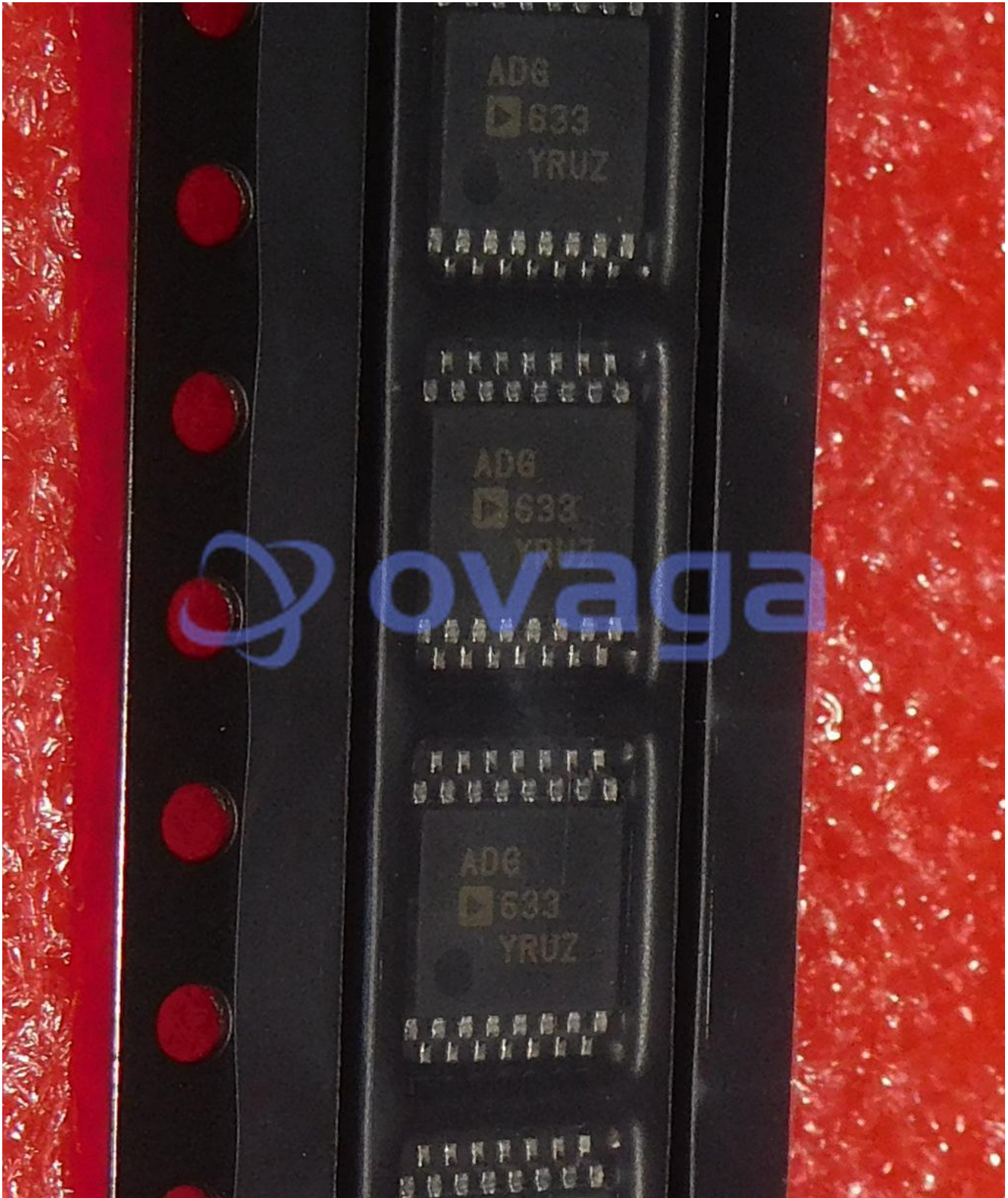
Communications systems

Audio and video signal routing

Relay replacement

Sample-and-hold systems

Industrial control systems



Related Products



[ADV7181CBSTZ](#)

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LQFP-64



[AD8170AR](#)

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SOP8



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LFCSP-VQ-40



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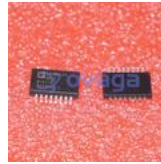
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