

SRAM Chip Sync Single 3.3V/5V 512K-bit 64K x 8 25ns 8-Pin SOIC N Tube

Manufacturers	Microchip Technology, Inc
Package/Case	SOIC-8
Product Type	Memory
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for 23LCV512-I/SN or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The Microchip Technology Inc. 23LCV512 are 512 Kbit Serial SRAM devices. The memory is accessed via a simple Serial Peripheral Interface (SPI) compatible serial bus. The bus signals required are a clock input (SCK) plus separate data in (SI) and data out (SO) lines. Access to the device is controlled through a Chip Select (CS) input. Additionally, SDI (Serial Dual Interface) is supported if your application needs faster data rates. The SRAM can be battery backed via the Vbat pin essentially making the SRAM non-volatile. The device also supports unlimited read and write cycles to the array. The 23LCV512 is available in standard packages including 8-lead SOIC, PDIP and advanced 8-lead TSSOP.

Related Products



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TSSOP-8



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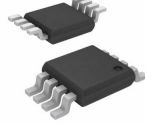
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