

CPLD CoolRunner XPLA3 Family 3K Gates 128 Macro Cells 119MHz 0.35um (CMOS) Technology 3.3V

Manufacturers	AMD Xilinx, Inc
Package/Case	100-TQFP
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for XCR3128XL-7VQG100I or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

XCR3128XL-7VQG100I is a field-programmable gate array (FPGA) manufactured by Xilinx.

Features

- It has a capacity of 128,000 logic cells, which can be configured as combinatorial or sequential logic.
- It has 3.3V I/O compatibility with 5V tolerance on selected pins.
- It has 288 user I/O pins and 8 global clock pins.
- It has built-in multipliers, digital signal processing (DSP) blocks, and RAM blocks for implementing complex digital signal processing algorithms.
- It supports a variety of communication protocols such as Ethernet, USB, PCI, and SPI.

Application

- High-speed data processing and signal processing applications
- Video and audio processing
- Communications and networking systems
- Industrial control and automation systems
- Aerospace and defense applications



Related Products



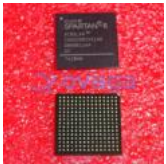
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AMD Xilinx, Inc
SOP-20



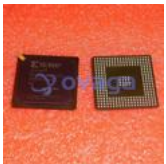
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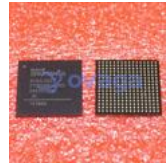
[XCF32PVO48C](#)

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