

CPLD XC9500 Family 6.4K Gates 288 Macro Cells 66.7MHz 0.5um (CMOS) Technology 5V



Images are for reference only

Manufacturers	AMD Xilinx, Inc
Package/Case	208-BFQFP Exposed Pad
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	

Please submit RFQ for XC95288-10HQ208C or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

XC95288-10HQ208C is a field-programmable gate array (FPGA) developed by Xilinx Inc., a leading manufacturer of programmable logic devices. The XC95288-10HQ208C is a high-performance, high-density FPGA that offers 288,000 system gates, 288 I/O pins, and 10ns pin-to-pin delay.

Features

288,000 system gates

288 I/O pins

10ns pin-to-pin delay

High-performance, high-density FPGA

JTAG programming and boundary-scan testing

3.3V power supply

-40°C to +100°C temperature range

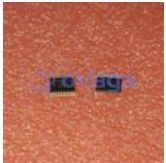


Related Products



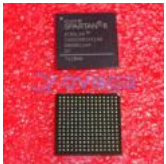
[XCF18V01S020C](#)

AMD Xilinx, Inc
SOP-20



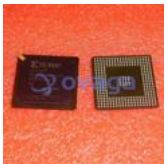
[XCF04SV0G20C](#)

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[XC6SLX4-2CSG225C](#)

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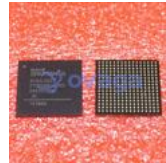
[XCV50-6BG256C](#)

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BGA256



[XCF08PV0G48C](#)

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[XC6SLX25-3FTG256C](#)

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[XC6SLX16-3CSG324C](#)

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BGA-324



[XCF32PVO48C](#)

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