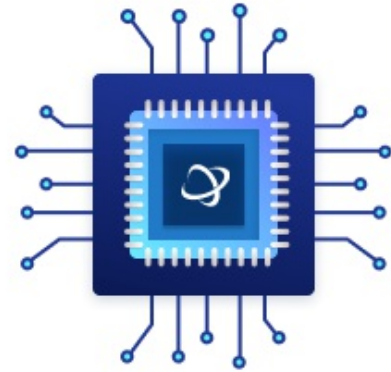


4-Output DB800ZL PCIe Zero-Delay/Fanout Clock Buffer

Manufacturers	Renesas Technology Corp
Package/Case	
Product Type	Clock & Timer ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for 9ZXL0451EKILFT or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The 9ZXL0451E is a second generation enhanced performance DB800ZL derivative for PCIe Gen4 and 5 applications. In fanout (bypass) mode, it is DB2000Q compatible. A fixed external feedback in ZDB mode maintains low drift for critical QPI/UPI.

Features

LP-HCSL outputs eliminate 16 resistors; save 32mm² of area

PCIe Gen 1–5 compliance

4 OE# pins; SMBus control also available

3 selectable SMBus addresses

2 selectable ZDB bandwidths; minimizes jitter peaking in cascaded PLL topologies

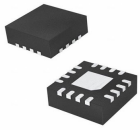
Hardware/SMBus control of ZDB bandwidth and fanout modes

Spread spectrum compatible

100MHz ZDB mode

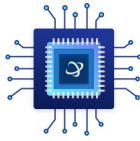
5 × 5 mm 32-VFQFPN package; small board footprint

Related Products



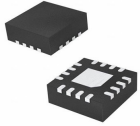
[9ZX21901CKLF](#)

Renesas Technology Corp
72-VFQFN



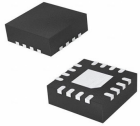
[9ZXL0851EKILFT](#)

Renesas Technology Corp



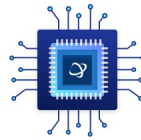
[9ZXL1950BKLF](#)

Renesas Technology Corp
72-VFQFN



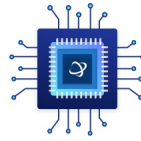
[9ZXL1930BKLF](#)

Renesas Technology Corp
72-VFQFN



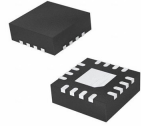
[9ZXL1251EKILFT](#)

Renesas Technology Corp



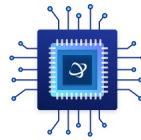
[9ZXL1950BKLFT](#)

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[9ZXL1930BKLFT](#)

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72-VFQFN



[9ZXL1550BKLF](#)

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