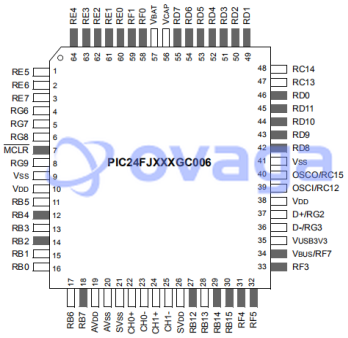
PIC24FJ64GC006-I/PT

Data Sheet

MCU 16-bit PIC RISC 64KB Flash 2.5V/3.3V Automotive 64-Pin TQFP Tray

Manufacturers	Microchip Technology, Inc
Package/Case	TQFP-64
Product Type	Embedded Processors & Controllers
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for PIC24FJ64GC006-I/PT or Email to us: sales@ovaga.com We will contact you in 12 hours. RFQ

General Description

PIC24F 16-bit Flash Microcontroller with low noise, high throughput, integrated Intelligent Analog ideal for portable & industrial applications. Segmented LCD, USB, and eXtreme Low Power consumption enable small footprint and long battery life.

Features

Integrated Analog Features

16-bit Sigma-Delta ADC (2 diff. ch) and 12-bit 10MSPS Pipeline ADC (50 ch)

2x10-bit 1MSPS DAC

2 Operational Amplifiers

3 Comparators

3 Voltage References

Charge Time Measurement Unit (CTMU)

eXtreme Low Power features

VBAT allows for lowest power consumption on backup battery (with or without RTCC)

18 nA Deep Sleep mode

350 nA RTCC in Vbat mode

380 nA Low Voltage Sleep mode (RAM retention)

400 nA Real Time Clock & Calendar operation in Sleep modes

240 nA Watch Dog Timer operation in Deep Sleep modes

180 μA/MHz Run mode

Power Modes: Run, Doze, Idle, Sleep, Low Voltage Sleep, Deep Sleep, Vbat

Multiple, flexible clock modes for optimum performance and power management

Deep Sleep Wake Sources: DSBOR, DSWDT, INT0 and RTCC

CPU

Up to 16 MIPS performance

Single Cycle Instruction Execution

16 x 16 Hardware Multiply, & 32-bit x 16-bit Hardware Divider

C Compiler Optimized Instruction Set Architecture

6 Channel DMA

Select Peripherals

USB with Device/Host/OTG support with Active Clock Tuning (no crystal required)

Segmented LCD driver with charge-pump, 59 seg. x 8 comm.

Peripheral Pin Select allows I/O remapping of many peripherals in real time

Hardware RTCC, Real-Time Clock Calendar with alarm system

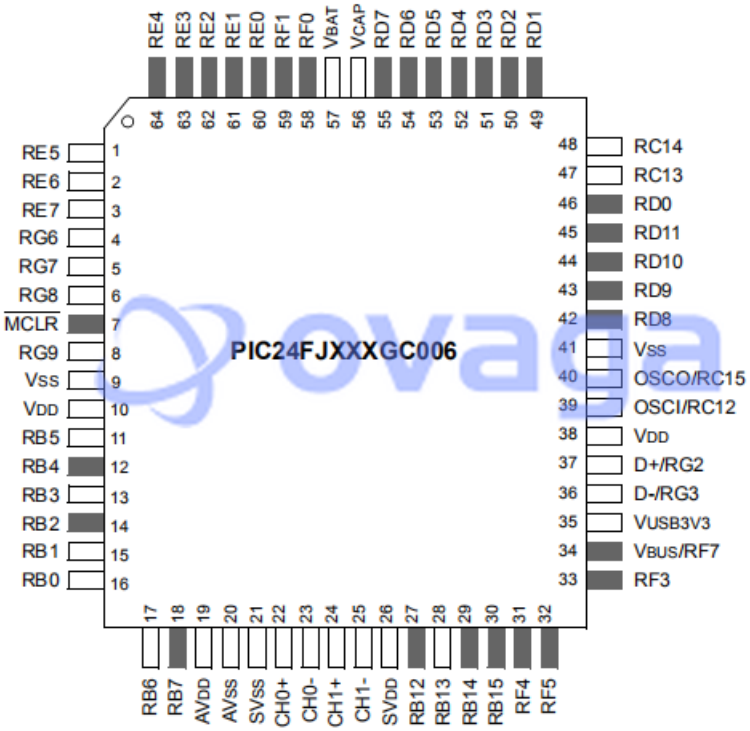
Internal oscillators support - 31 kHz to 8 MHz, up to 32 MHz with 96 MHz PLL

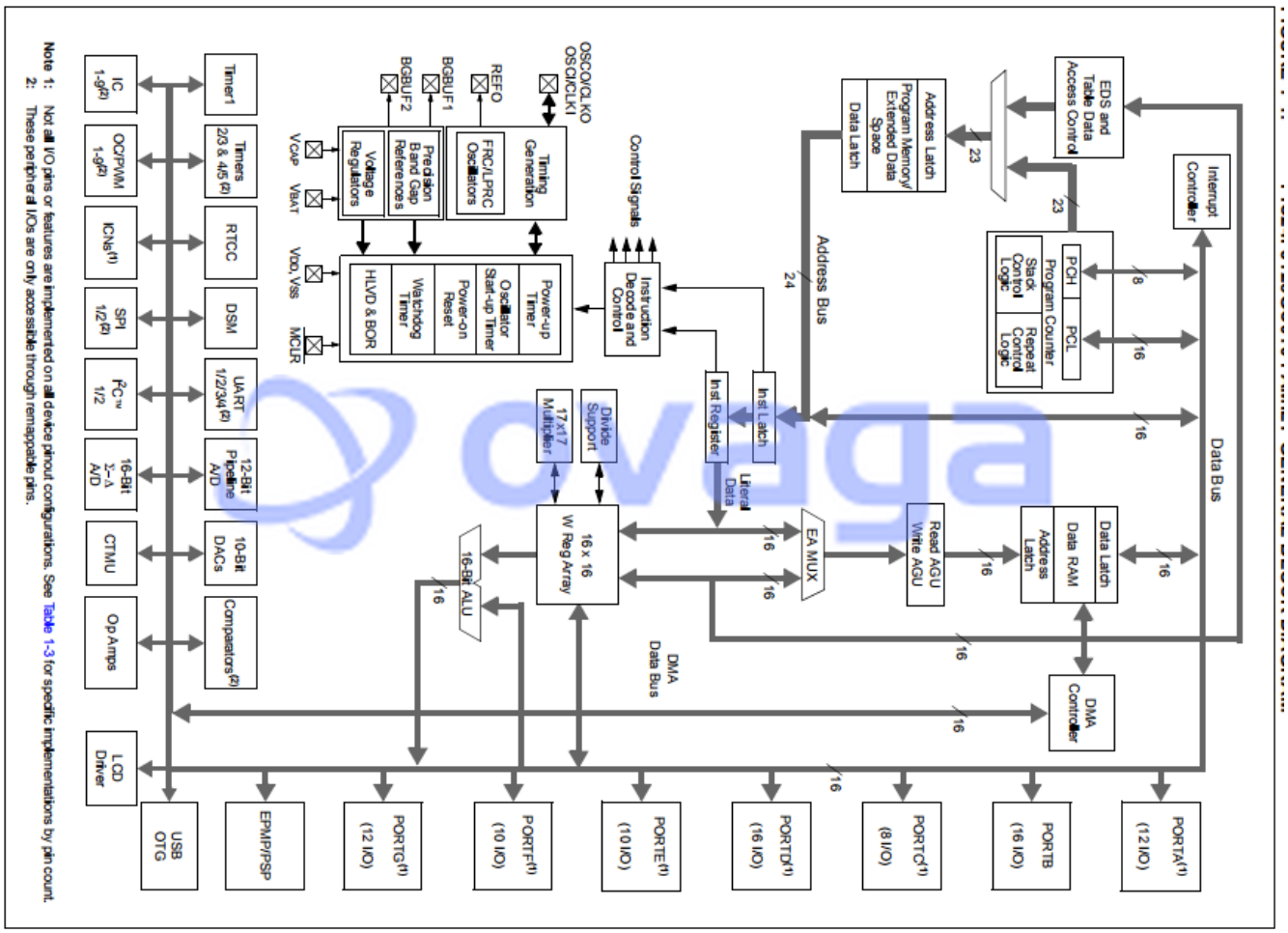
Fail-Safe Clock Monitor - allows safe shutdown if clock fails

Watchdog Timer with separate RC oscillator

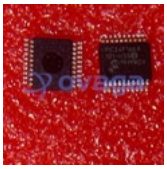
System Supervisors: Low Power BOR, WDT, INT0 and RTCC

JTAG Boundary Scan





Related Products



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SSOP-28

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PDIP-28

[PIC18F23K22-I/SP]

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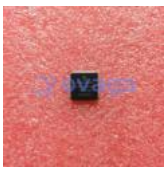
SPDIP-28



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TQFP-64

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TQFP-100