

288 MACROCELL 5 VOLT ISP CPLD - NOT RECOMMENDED for NEW DESIGN

Manufacturers	AMD Xilinx, Inc
Package/Case	208-BFQFP Exposed Pad
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for XC95288-15HQ208C or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

RFQ

General Description

XC95288-15HQ208C is a programmable logic device (PLD) from the Xilinx XC9500 family, which is a series of high-performance CPLDs (Complex Programmable Logic Devices) based on EEPROM technology.

Features

288 macrocells

5 ns pin-to-pin delay

15 ns tPD (max) for combinatorial paths

5,000 to 100,000 gates (equivalent)

3.3V or 2.5V operation

High speed interface support

In-system programmable (ISP) via IEEE 1532 compliant boundary scan

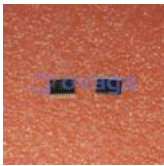


Related Products



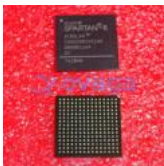
[XCF18V01S020C](#)

AMD Xilinx, Inc
SOP-20



[XCF04SV0G20C](#)

AMD Xilinx, Inc
TSSOP20



[XC6SLX4-2CSG225C](#)

AMD Xilinx, Inc
BGA-225



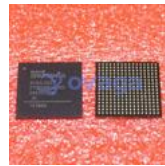
[XCV50-6BG256C](#)

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