

Encoders, Decoders, Multiplexers & Demultiplexers 1:20 LVPECL Fanout w/ 2:1 FSI Input MUX

Manufacturers	Microchip Technology, Inc
Package/Case	TQFP-64
Product Type	Clock & Timer ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for SY89467UHY or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The SY89467U is a 2.5/3.3V, 1:20 LVPECL fanout buffer with a 2:1 differential input multiplexer (MUX). A unique Fail-Safe Input (FSI) protection prevents metastable output conditions when the selected input clock fails to a DC voltage (when voltage between the pins of the differential input drops significantly below 100mV). The differential input includes Micrel's unique, 3-pin internal termination architecture that can interface to any differential signal (AC- or DC-coupled) as small as 100mV (200mVPP) without any level shifting or termination resistor networks in the signal path. The outputs are 800mV, LVPECL with fast rise/fall times guaranteed to be less than 270ps. The SY89467U operates from a 2.5V \pm 5% or 3.3V \pm 10% supply and is guaranteed over the full industrial temperature range of -40°C to +85°C. The SY89467U is part of Micrel's high-speed, Precision Edge® product line.

Features

Selects between two inputs, and provides 20 precision LVPECL copies

Fail-Safe Input

Prevents outputs from oscillating when input is invalid

Guaranteed AC performance over temperature and supply voltage:

DC-to >1.5 GHz throughput

270ps Rise/Fall times

Ultra-low jitter design:

Unique, patented MUX input isolation design minimizes adjacent channel crosstalk

Unique patented internal termination and VT pin accepts DC- and AC-coupled inputs (CML, PECL, LVDS)

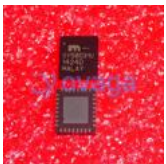
Wide input voltage range: VCC to GND

2.5V \pm 5% or 3.3V \pm 10% supply voltage

Available in 64-pin TQFP package



Related Products



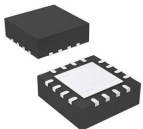
[SY58031UMG](#)

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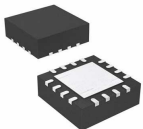
[SY89833LMG](#)

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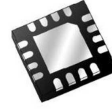
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