

High-Speed, +3.3V, 2-Channel RS232/V.28 Interface Device with 460kBPS Data Rate and Shutdown and Enable Pins

Manufacturers	Analog Devices, Inc
Package/Case	SOIC-18
Product Type	Interface ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for ADM3222ARWZ or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The ADM3202/ADM3222/ADM1385 conform to the EIA-232E and CCITT V.28 specifications and operate at data rates up to 230 kbps. Four external 0.1 μ F charge pump capacitors are used for the voltage doubler/inverter permitting operation from a single +3.3 V supply.

The ADM3222/ADM1385 contain additional enable and shutdown circuitry. The EN input may be used to three-state the receiver outputs. The SD input is used to power down the charge pump and transmitter outputs reducing the quiescent current to less than 1 μ A. The receivers remain enabled during shutdown unless disabled using EN.

The ADM3202 is available in a 16-lead DIP, narrow and wide SO as well as a space saving TSSOP package. The ADM3222 is available in 18 lead DIP, SO and in 20 lead SSOP and TSSOP. The ADM1385 is available in a 20 lead SSOP package and is pin compatible with the LTC1385 CG.

Features

460 kbps Data Rate

Specified at 3.3 V

Meets EIA-232E Specifications

0.1 μ F Charge Pump Capacitors

Low Power Shutdown (ADM3222E and ADM1385)

Upgrade for MAX3222/32 and LTC1385

DIP, SO, SOIC, SSOP and TSSOP Package Options

ESD Protection to IEC1000-4-2 (801.2) on RS-232 Pins (ADM3202 Only) \pm 8 kV: Contact Discharge \pm 15 kV: Air-Gap Discharge

Related Products



[ADV7181CBSTZ](#)

Analog Devices, Inc
LQFP-64



[AD8170AR](#)

Analog Devices, Inc
SOP8



[AD724JR](#)

Analog Devices, Inc
SOIC-16



[ADV7393BCPZ](#)

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LFCSP-VQ-40



[ADV7391WBCPZ](#)

Analog Devices, Inc
LFSCP-3



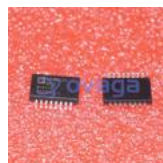
[ADV7390BCPZ](#)

Analog Devices, Inc
QFN32



[ADV7341BSTZ](#)

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[ADUM4160BRIZ](#)

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SOIC-16