

CPLD XC9500 Family 1.6K Gates 72 Macro Cells 55.6MHz 0.5um Technology 5V 84Pin PLCC



Images are for reference only

Manufacturers	AMD Xilinx, Inc
Package/Case	PLCC84
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	

Please submit RFQ for XC9572-15PCG84C or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

XC9572-15PCG84C is a programmable logic device (PLD) from the XC9500 series of CPLDs (Complex Programmable Logic Devices) manufactured by Xilinx. It has 72 macrocells and operates on a 5V power supply with a maximum frequency of 33 MHz. The "15" in its part number refers to its speed grade, which has a maximum propagation delay of 15 ns.

Features

- 72 macrocells with 64 logic gates per macrocell
- 84-pin plastic leaded chip carrier (PLCC) package
- 5V power supply with TTL-compatible inputs and outputs
- Maximum frequency of 33 MHz
- In-system programmability using JTAG interface
- Non-volatile storage of configuration data

Application

- ATF1502AS-15JC44
- GAL22V10D-15LPN
- MAX7000AEPI-15
- EPM7064STC44-15
- LC4064V-75T100I



Related Products



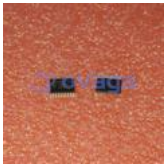
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AMD Xilinx, Inc
SOP-20



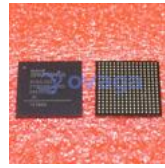
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TSOP-48



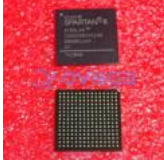
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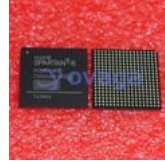
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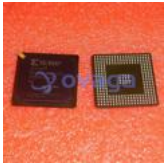
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BGA-225



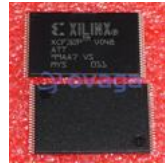
[XC6SLX16-3CSG324C](#)

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[XCV50-6BG256C](#)

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