600,000 SYSTEM GATE 1.8 VOLT FPGA - NOT RECOMMENDED for NEW DESIGN

Manufacturers	AMD Xilinx, Inc
Package/Case	BGA456
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	

Images are for reference only

Please submit RFQ for XC2S600E-6FG456I or Email to us: sales@ovaga.com We will contact you in 12 hours.

RFQ

General Description

XC2S600E-6FG456I is a Field-Programmable Gate Array (FPGA) manufactured by Xilinx. Here are some of its features:

Features

The device belongs to the Spartan-II family of Xilinx FPGAs.

It has a density of 600,000 system gates, which means that it contains a large number of configurable logic blocks (CLBs), digital signal processing (DSP) blocks, and other components that can be programmed to implement a wide range of digital circuits and systems.

It operates at a maximum clock frequency of 133 MHz, which allows for high-speed digital signal processing and other real-time applications.

It has 456 pins arranged in a Fine Pitch Ball Grid Array (FBGA) package, which provides a high-density and low-profile packaging solution.

Application

High-speed digital signal processing (DSP) applications such as digital filters, FFTs, and other signal processing algorithms.

Embedded systems applications that require a high level of integration and flexibility, such as video processing, network switching, and control systems.

Aerospace and defense applications that require high reliability, radiation tolerance, and low power consumption.
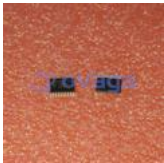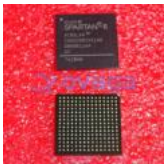
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