

CPLD MAX 3000A Family 10K Gates 512 Macro Cells 116.3MHz CMOS Technology
3.3V 208Pin PQFP

| | |
|---------------|--|
| Manufacturers | Altera Corporation (Intel) |
| Package/Case | PQFP-208 |
| Product Type | Programmable Logic ICs |
| RoHS | |
| Lifecycle | |



Images are for reference only

Please submit RFQ for EPM3512AQC208-7 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

EPM3512AQC208-7 is a part number for a programmable logic device (PLD) manufactured by Intel (formerly Altera), a company specializing in semiconductor technology. It belongs to the MAX 3000A family of PLDs.

Features

Package: Quad Flat Pack (QFP) with 208 pins
EPM3512AQC208-7 can be used in various digital logic applications such as data processing, control systems, communication systems, automotive electronics, industrial automation, and more.

Logic elements: 3,000 equivalent macrocells

Internal memory: 12,000 bits of EAB (Embedded Array Block) memory

Maximum user I/O pins: 160

Maximum user usable gates: 12,000 gates

Programmable interconnects: 12,000

Clocks: Four global clock lines

Voltage: 3.3V

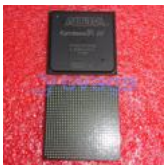
Speed grade: 7 (denoted by the "-7" in the part number), indicating a maximum frequency of 7 MHz

Application

It is commonly used for tasks that require programmable digital logic functions, such as in FPGA (Field-Programmable Gate Array) designs, where it can be configured to perform specific tasks or functions based on the user's requirements.

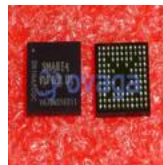


Related Products



[EP4CE55F29C8N](#)

Altera Corporation (Intel)
FBGA-780



[EPM240M100C5N](#)

Altera Corporation (Intel)
BGA-100



[EPM1270T144A5N](#)

Altera Corporation (Intel)
TQFP-144



[EPM570F256C5N](#)

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FBGA-256



[EP2C35F672C8N](#)

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[EPM7128AETC100-10](#)

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TQFP-100



[EP2C35F484C7N](#)

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FBGA-484



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FBGA-484