

SRAM Chip Async Single 3V 16M-Bit 2M/1M x 8/16Bit 55ns 48Pin TSOP-I

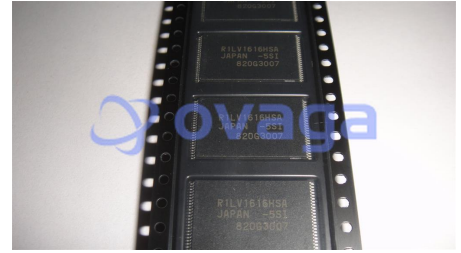
Manufacturers [Renesas Technology Corp](#)

Package/Case TSOP-48

Product Type Memory

RoHS

Lifecycle



Images are for reference only

Please submit RFQ for R1LV1616HSA-5SI#B0 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

Features

Single 3.0 V supply: 2.7 V to 3.6 V

Fast access time: 45/55 ns (max)

Power dissipation

-Active: 9 mW/MHz (typ)

-Standby: 1.5 μ W (typ)

Completely static memory.

-No clock or timing strobe required

Equal access and cycle times

Common data input and output.

-Three state output

Battery backup operation.

-2 chip selection for battery backup

Temperature range: -40 to +85°C

Byte function (x8 mode) available by BYTE# & A-1

Embedded ECC (error checking and correction) for single-bit error correction



Related Products



[R1LP0408DSP-7SI#B0](#)

Renesas Technology Corp
SOP-32



[R1LP0108ESF-7SI#B0](#)

Renesas Technology Corp
32-TFSOP (0.724, 18.40mm Width)



[R1LV1616RSA-5SI#B0](#)

Renesas Technology Corp
TSOP48



[R1LV0408DSA-5SI#S0](#)

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TSOP32



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[R1LV1616HSA-4SI#B0](#)

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[R1LV1616RSD-5SI#B0](#)

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[R1WV3216RBG-7SI#B0](#)

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TFBGA-48