

Digital to Analogue Converter, 16 bit, 167 kSPS, Parallel, $\pm 13.5V$ to $\pm 16.5V$, DIP, 28 Pins

Manufacturers	Analog Devices, Inc
Package/Case	PDIP-28
Product Type	Data Conversion ICs
RoHS	Pb-free Halide free
Lifecycle	



Images are for reference only

Please submit RFQ for AD669ANZ or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The AD669 DACPORT® is a complete 16-bit monolithic D/A converter with an on-board reference and output amplifier. It is manufactured on Analog Devices' BiMOS II process. This process allows the fabrication of low power CMOS logic functions on the same chip as high precision bipolar linear circuitry. The AD669 chip includes current switches, decoding logic, an output amplifier, a buried Zener reference and double-buffered latches.

The AD669's architecture insures 15-bit monotonicity over temperature. Integral nonlinearity is maintained at $\pm 0.003\%$, while differential nonlinearity is $\pm 0.003\%$ max. The on-chip output amplifier provides a voltage output settling time of 10 ms to within 1/2 LSB for a full-scale step.

Data is loaded into the AD669 in a parallel 16-bit format. The double-buffered latch structure eliminates data skew errors and provides for simultaneous updating of DACs in a multi-DAC system. Three TTL/LSTTL/5 V CMOS compatible signals control the latches: CS, L1 and LDAC.

The output range of the AD669 is pin programmable and can be set to provide a unipolar output range of 0 V to +10 V or a bipolar output range of -10 V to +10 V.

The AD669 is available in seven grades: AN and BN versions are specified from $-40^{\circ}C$ to $+85^{\circ}C$ and are packaged in a 28-pin plastic DIP. The AR and BR versions are specified for $-40^{\circ}C$ to $+85^{\circ}C$ operation and are packaged in a 28-pin SOIC. The SQ version is specified from $-55^{\circ}C$ to $+125^{\circ}C$ and is packaged in a hermetic 28-pin cerdip package. The AD669 is also available compliant to MIL-STD-883. Refer to the AD669/883B data sheet for specifications and test conditions.

Features

Complete 16-Bit D/A Function On-Chip Output Amplifier High Stability Buried Zener Reference

Monolithic BiMOS II Construction

15-Bit Monotonic over Temperature

Fast 40 ns Write Pulse

Microprocessor Compatible 16-Bit Parallel Input Double-Buffered Latches

Unipolar or Bipolar Output

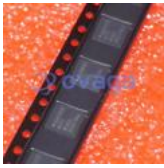
Low Glitch: 15 nV-s

Low THD+N: 0.009%

MIL-STD-883 Compliant Versions Available

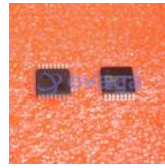


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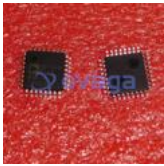
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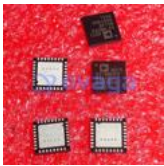
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