

512 MACROCELL 1.8V ZERO POWER ISP CPLD

Images are for reference only

Manufacturers

[AMD Xilinx, Inc](#)

Package/Case

QFP208

Product Type

Programmable Logic ICs

RoHS

Lifecycle

Please submit RFQ for XC2C512-10PQ208I or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

XC2C512-10PQ208I is a field-programmable gate array (FPGA) device from the Xilinx CoolRunner-II family.

Features

Logic cells: 512

Maximum frequency: 142 MHz

Operating voltage: 1.8V

Programmable interconnects: 4,608

Block RAM: 18 Kb

Digital Clock Managers (DCMs): 4

Application

Automotive electronics

Industrial control systems

Consumer electronics

Communications equipment

Medical devices

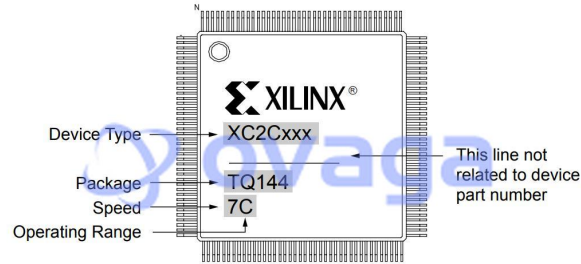
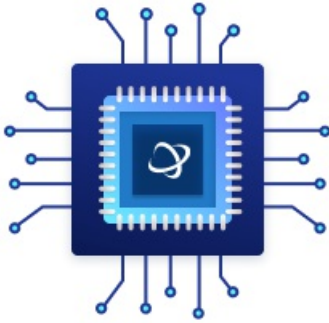


Figure 5: Sample Package with Part Marking

Related Products



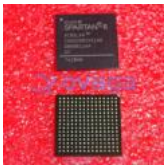
[XCV50-6BG256C](#)

AMD Xilinx, Inc
BGA256



[XCF04SV0G20C](#)

AMD Xilinx, Inc
TSSOP20



[XC6SLX4-2CSG225C](#)

AMD Xilinx, Inc
BGA-225



[XCF32PVO48C](#)

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BGA256



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