

Field Programmable Gate Arrays

Manufacturers	AMD Xilinx, Inc
Package/Case	QFP240
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for XCV200-6PQ240I or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

XCV200-6PQ240I is a type of field-programmable gate array (FPGA) from the Xilinx Virtex-II Pro family. It is a high-performance programmable logic device with 200,000 logic cells, 360 multipliers, and 576kb of block RAM. The "6" in the part number refers to the speed grade, which is -6, indicating a maximum operating frequency of 250MHz.

Features

200,000 logic cells

360 multipliers

576kb block RAM

Maximum operating frequency of 250MHz

Support for up to 48 high-speed serial transceivers

Support for multiple I/O standards, including LVCMOS, LVTTTL, HSTL, SSTL, and differential signaling standards

Built-in PowerPC 405 processor core

Advanced on-chip clock management circuitry

Application

XC2VP20-6FF1152C

XC2VP30-6FF1152C

XC2VP40-6FF1152C

XC2VP50-6FF1152C

XC2VP70-6FF1704C



Related Products



[XC18V01S020C](#)

AMD Xilinx, Inc
SOP-20



[XCF04SV0G20C](#)

AMD Xilinx, Inc
TSSOP20



[XC6SLX4-2CSG225C](#)

AMD Xilinx, Inc
BGA-225



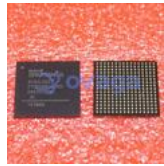
[XCV50-6BG256C](#)

AMD Xilinx, Inc
BGA256



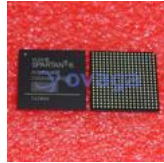
[XCF08PV0G48C](#)

AMD Xilinx, Inc
TSOP-48



[XC6SLX25-3FTG256C](#)

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BGA-256



[XC6SLX16-3CSG324C](#)

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BGA-324



[XCF32PVO48C](#)

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TSOP48