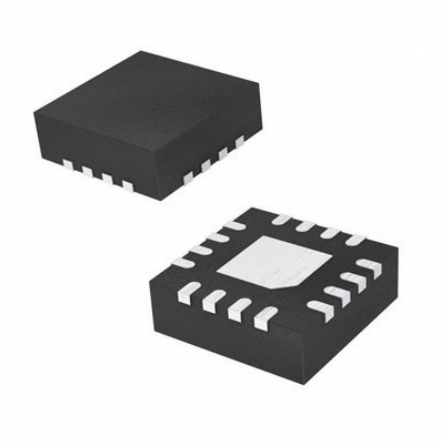
ZL40214LDG1

Data Sheet

Clock Fanout Buffer 4Out 1IN 1:4 16Pin QFN EP Tray

Manufacturers	Microchip Technology, Inc
Package/Case	16-VFQFN
Product Type	Clock & Timer ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for ZL40214LDG1 or Email to us: sales@ovaga.com We will contact you in 12 hours. RFQ

General Description

The ZL40214 is an LVDS clock fanout buffer with four identical output clock drivers capable of operating at frequencies up to 750MHz.

Inputs to the ZL40214 are externally terminated to allow use of precision termination components and to allow full flexibility of input termination. The ZL40214 can accept DC coupled LVPECL or LVDS and AC coupled LVPECL, LVDS, CML or HCSL input signals; single ended input signals can also be accepted. A pin compatible device with internal termination is also available.

The ZL40214 is designed to fan out low-jitter reference clocks for wired or optical communications applications while adding minimal jitter to the clock signal. An internal linear power supply regulator and bulk capacitors minimize additive jitter due to power supply noise. The device operates from 2.5V+/-5% or 3.3V+/-5% supply. Its operation is guaranteed over the industrial temperature range -40°C to +85°C.

Features

Ultra low additive jitter of 92 fs RMS

Accepts differential or single-ended input: LVPECL, LVDS, CML, HCSL, LVCMOS

Four precision LVDS outputs

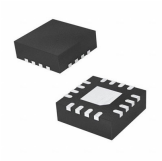
Operating frequency up to 750 MHz

Options for 2.5 V or 3.3 V power supply

On-chip Low Drop Out (LDO) Regulator for superior power supply noise rejection

Current consumption of 61 mA

Related Products



[ZL30267LDG1]

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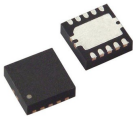
56-VFQFN



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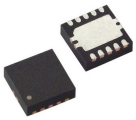
VQFN



[ZL40253LDG1Q06M]

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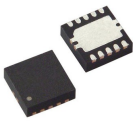
VQFN



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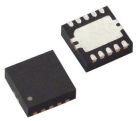
VQFN



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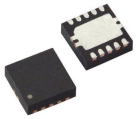
VQFN



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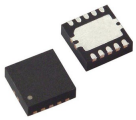
VQFN



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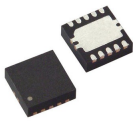
VQFN



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